

*(Use several sheets if necessary)*

**OKA-C062**

**09-808,016**

**APPLICANT(S)**  
**MAEDA et al**

**March 15, 2001**

GROUP	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100	

[illegible]

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
1AK		6-267939	9/22/94	Japan				
1AK		1 058 301 A1	2/12/99	Europe				
1AK		9-205089	8/5/97	Japan				
1AK		5-343394	12/24/93	Japan				
1AK		9-148324	6/6/97	Japan				


Tracy

4/16/02

Form DTG A970

P09C/REV03

Patent and Trademark Office \* U.S. DEPARTMENT OF COMMERCE

**INFORMATION DISCLOSURE CITATION**  
(Use several sheets if necessary)

ATTY DOCKET NO.  
**OKA-C062**

SERIAL NO.  
**09-808,016**

APPLICANT(S)  
**MAEDA et al**

FILING DATE  
**March 15, 2001**

GROUP

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>CSK</i>	5,656,337	8/12/97	Park et al	437	238	—
<i>CSK</i>	5,470,800	5/26/93	Muroyama	427	539	—
<i>LNU</i>	5,424,253	6/13/95	Usami	437	238	—
<i>UAK</i>	5,605,867	2/25/97	Sato et al	437	235	—

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
<i>CSK</i>	7-50295	2/21/95	Japan	—	—	—	—
<i>CSK</i>	5-259297	10/8/93	Japan	—	—	—	—
<i>CSK</i>	7-99191	4/11/95	Japan	—	—	—	—
<i>CSK</i>	7-122552	5/12/95	Japan	—	—	—	—
<i>CSK</i>	7-29901	1/31/95	Japan	—	—	—	—

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>AK</i>			"Silicon Processing for the VLSI Era, Volume 2 - Process Integration. By Stanley. Wolf PhD. pp 196-199

EXAMINER

*2:AK*

DATE CONSIDERED

*4/16/02*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY DOCKET NO.

OKA-C062

SERIAL NO.

09-808,016

APPLICANT(S)

MAEDA et al

FILING DATE

March 15, 2001

GROUP

## U.S. PATENT DOCUMENTS

EXAMINER  
INITIAL

DOCUMENT NUMBER

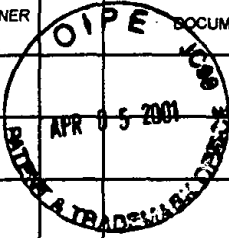
DATE

NAME

CLASS

SUBCLASS

FILING DATE  
IF APPROPRIATE



## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER

DATE

COUNTRY

CLASS

SUBCLASS

TRANSLATION  
YES NO

LAL

6-181205

6/28/94

Japan

LAL

9-129632

5/16/97

Japan

LAL

11-74485

3/16/99

Japan

LAL

11-288933

10/19/99

Japan

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

*Li Ali*

DATE CONSIDERED

*4/16/02*

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.